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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/575,028	06/19/2006	Guangwei Wu	TWI-039	5936
25199	7590	11/17/2008	EXAMINER	
LARRY WILLIAMS			VINH, LAN	
3645 MONTGOMERY DR			ART UNIT	PAPER NUMBER
SANTA ROSA, CA 95405-5212			1792	
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Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

Office Action Summary	Application No. 10/575,028	Applicant(s) WU ET AL.
	Examiner LAN VINH	Art Unit 1792

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --
Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
 - If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
 - Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED. (35 U.S.C. § 133).
- Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) Responsive to communication(s) filed on 01 August 2008.
- 2a) This action is FINAL. 2b) This action is non-final.
- 3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) Claim(s) 1-20 is/are pending in the application.
- 4a) Of the above claim(s) 1-4 and 20 is/are withdrawn from consideration.
- 5) Claim(s) _____ is/are allowed.
- 6) Claim(s) 5-19 is/are rejected.
- 7) Claim(s) _____ is/are objected to.
- 8) Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) The specification is objected to by the Examiner.
- 10) The drawing(s) filed on _____ is/are: a) accepted or b) objected to by the Examiner.
 Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
 Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) All b) Some * c) None of:
1. Certified copies of the priority documents have been received.
 2. Certified copies of the priority documents have been received in Application No. _____.
 3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|------------------------------------------------------------------------------------------------------------------------------|-------------------------------------------------------------------|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413) |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | Paper No(s)/Mail Date. _____ |
| 3) <input checked="" type="checkbox"/> Information Disclosure Statement(s) (PTO-1448)
Paper No(s)/Mail Date <u>081606</u> | 5) <input type="checkbox"/> Notice of Informal Patent Application |
| | 6) <input type="checkbox"/> Other: _____ |

DETAILED ACTION

Election/Restrictions

1. Applicant's election with traverse of Group II, claims 5-19 in the reply filed on 8/1/2008 is acknowledged. The traversal is on the ground(s) that : 1. The present patent application is a national phase United States application filed under 35 USC 371 from International Application PCT/US2004/033591. In Chapter 1 of the PCT process, a search was performed for all of the claims and an international 10 search report was prepared. The claims in the PCT application are identical to the claims considered in making the restriction requirement. A copy of the international search report was submitted in an IDS for the instant application on 08/10/2006. At no time during the PCT international phase was there any indication that the number of inventions covered by the claims was unacceptable. It is Applicant's understanding that the claims met the requirements for unity of invention. Consequently, the present restriction requirement is inappropriate. This argument is unpersuasive because it is noted that section 1893.03(d) of the MPEP states: "Unity of Invention
37 CFR 1.499. Unity of invention during the national stage. If the examiner finds that a national stage application lacks unity of invention under § 1.475 , the examiner may in an Office action require the applicant in the response to that action to elect the invention to which the claims shall be restricted. Such requirement may be made before any action on the merits but may be made at any time before the final action at the discretion of the examiner.

2. The Office Action suggests that Shih et al., US 6,409,587 discloses a pad that is 20 substantially the same as Applicant's pad. However, the summary of Shih et al.'s teachings as presented in the Office Action is not sufficiently complete and leads to an inappropriate conclusion. A more detailed analysis shows that Shih et al. teaches a dual-hardness polishing pad for use in linear chemical mechanical polishing. The first material of Shih et al.'s pad may have a hardness not lower than 60 and the second 25 material may have a hardness not higher than 50. Shih et al.'s teachings are clearly different from the teachings for one of Applicant's preferred embodiments. According to one of Applicant's preferred embodiments, the top pad has a Shore D hardness of about 50-60 and the subpad has a Shore D hardness of about 50-60. This argument is unpersuasive because in one embodiment, the applicants discloses "More generally, for embodiments of the present invention top pad 20 has Shore D hardness greater than about 30, preferably from about 40 to about 70 and all ranges and values subsumed therein. Similarly, subpad 40 has a preferred Shore D hardness from about 30 to about 70 and all ranges" (page 5 of the instant application). Thus, Shih teaching of , the first material of the pad/top pad have a hardness not lower than 60 and the second material/substrate pad may have a hardness not higher than 50, meets the requirement of the sub pad having a Shore D hardness substantially equal to the hardness of the top pad as described in the instant specification

The requirement is still deemed proper and is therefore made FINAL.

2. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(a) the invention was known or used by others in this country, or patented or described in a printed publication in this or a foreign country, before the invention thereof by the applicant for a patent.

Claims 5, 12 are rejected under 35 U.S.C. 102(a) as being anticipated by Tsai et al (US 2003/0013387)

Tsai discloses a method for planarizing a substrate. The method comprises providing a substrate having a surface for fabricating electronic devices, the surface comprising a dielectric material having a dielectric constant less than two (page 6, paragraph 0057)

providing a stacked pad, the stacked pad comprising a hard polishing material/top pad having a Shore D hardness of 50 or greater and a soft polishing material/subpad having a Shore D hardness of less than 50 (page 8, paragraphs 0082-0085), which reads on providing a stacked pad , the stacked pad comprising a top pad having a Shore D hardness from about 40 to about 70 and a subpad having a Shore D hardness substantially equal to the hardness of the top pad since the applicants discloses "More generally, for embodiments of the present invention top pad 20 has Shore D hardness greater than about 30, preferably from about 40 to about 70 and all ranges and values subsumed therein. Similarly, subpad 40 has a preferred Shore D hardness from about 30 to about 70 and all ranges" (page 5 of the instant application).

contacting the top pad with the surface and planarizing the surface with the stacked pad (page 8, paragraph 0088)

Regarding claim 12, Tsai discloses polishing substrates/plurality of wafers (page 2, paragraph 0014)

3. Claim 15 is rejected under 35 U.S.C. 102(a) as being anticipated by Tsai et al (US 2003/0013387)

Tsai discloses a method for planarizing a substrate. The method comprises providing a substrate having a surface for fabricating electronic devices (page 6, paragraph 0057)

providing a stacked pad, the stacked pad comprising a hard polishing material/top pad having a Shore D hardness of 50 or greater and a soft polishing material/subpad having a Shore D hardness of less than 50 (page 8, paragraphs 0082-0085), which reads on providing a stacked pad, the stacked pad comprising a top pad and a subpad, wherein the hardness or modulus of the top pad substantially equals the hardness or modulus of the subpad since the applicants discloses "More generally, for embodiments of the present invention top pad 20 has Shore D hardness greater than about 30, preferably from about 40 to about 70 and all ranges and values subsumed therein. Similarly, subpad 40 has a preferred Shore D hardness from about 30 to about 70 and all ranges" (page 5 of the instant application).

contacting the top pad with the surface and planarizing the surface with the stacked pad (page 8, paragraph 0088)

Claim Rejections - 35 USC § 103

4. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

Claims 6-11, 13-14 are rejected under 35 U.S.C. 103(a) as being unpatentable over Tsai et al (US 2003/0013387) in view of Roberts et al (US 2004/0048564)

Tsai method has been described above. Unlike the instant claimed inventions as per claims 6-10, 14, Tsai fails to specifically disclose a step of conditioning the top pad using a down force less than about 0.24 psi (1.7 KPa) after planarization of a plurality of the substrates (5 substrates)/repeating the polishing steps, the stacked pad is conditioned on a tool other than a polishing tool

Robert discloses a method for polishing comprises a step of conditioning a polishing pad using a down force of 0.1-25 psi after planarization of a plurality of wafers, the pad is conditioned with a disk/ a tool other than a polishing tool (page 5, paragraphs 0072-0074)

One skilled in the art at the time the invention was made would have found it obvious to modify Tsai method to include a step of conditioning a polishing pad using a down force of 0.1 psi after planarization of a plurality of wafers as per Robert to reform the micro-protrusions on the polishing pad in order to prevent diminishing polishing performance (page 5, paragraph 0072) and to extend the life of the polishing pad.

Unlike the instant claimed inventions as per claims 11, 13, Tsai fails to disclose

Art Unit: 1792

conditioning the top pad only prior to the first planarization. Robert also teaches that pad are generally conditioned prior to use (page 3, paragraph 0029). Ones skilled in the art at the time the invention was made would have also found it obvious to modify Tsai method to include a step of conditioning the top pad only prior to the first planarization to create or augment the micro texture of the pad as taught per Robert (paragraph 0029)

5. Claims 16-19 are rejected under 35 U.S.C. 103(a) as being unpatentable over Tsai et al (US 2003/0013387) in view of Chen et al (US 6,712,681)

Tsai method has been described above. Unlike the instant claimed inventions as per claims 17-18, 19, Tsai fails to specifically disclose that the top pad and subpad have a substantially equal density and the density is in the range from about 0.5 to about 0.7 grams/cc/the top pad and subpad have a substantially equal pore size range and the pore size range is in the range from about 0.5 to about 0.7 grams/cc

Chen discloses a method for fabricating polishing pad, the pad having multiple layer, having density of about 0.5 g/c , the pad also have pore sizes (col 5, lines 20-38; col 6, lines 18-21)

One skilled in the art at the time the invention was made would have found it obvious to modify Tsai method by using a top pad and subpad have a substantially equal density and the density is in the range from about 0.5/ a top pad and subpad have a substantially equal pore size range to ensure the desired hardness and uniform porosity

Art Unit: 1792

of the pad in order to achieve better polishing uniformity as taught in Chen (col 5, lines 20-27)

Regarding claim 16, since Tsai discloses that hard polishing material /top pad having a Shore D hardness of 50 or greater and a soft polishing material/subpad having a Shore D hardness of less than 50 , one skilled in the art at the time the invention was made would have found it obvious to compute/to optimize the value of the compressibility/ratio of hardness of the top pad and subpad by varying the hardness values of the top pad and subpad through routine experimentation

Conclusion

6. Any inquiry concerning this communication or earlier communications from the examiner should be directed to LAN VINH whose telephone number is (571)272-1471. The examiner can normally be reached on M-F 8:30-5:30 PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Nadine Norton can be reached on 571 272 1465. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Art Unit: 1792

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

/Lan Vinh/
Primary Examiner, Art Unit 1792